



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Shibaek Nam, et al.
Title: Semiconductor Device Having Multi-Chip Package Structure
Application No.: 10/038,714
Filing Date: January 2, 2002
Examiner: Pershelle L. Greene
Group Art Unit:: 2826
Confirmation No.: 8922
Law Office: Sidley Austin Brown & Wood LLP
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call the following documents (copies enclosed) to the attention of the Examiner for the above-identified patent application.

A form PTO-1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or

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3. an admission that the information listed herein is, or is considered to be, material to patentability as defined in § 1.56(b).

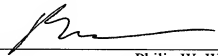
Please charge Deposit Account No. 50-1597 in the amount of \$180.00 for the Information Disclosure Statement fee under 37 C.F.R. § 1.17(p), as set forth in the accompanying transmittal letter. The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-1597.

EXPRESS MAIL LABEL NO.:

EV 305 257 695 US

Respectfully submitted,

By:



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August 21, 2003
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